

AUTOMOTIVE CURRENT TRANSDUCER HC6F300-S









Introduction

The HC6F family is for the electronic measurement of DC, AC or pulsed currents in high power and low voltage automotive applications with galvanic isolation between the primary circuit (high power) and the secondary circuit (electronic circuit).

The HC6F family gives you the choice of having different current measuring ranges in the same housing.

Features

- · Open Loop transducer using the Hall effect
- Low voltage application
- Unipolar + 5 V DC power supply
- Primary current measuring range from 200 A up to 800 A
- Maximum rms primary current limited by the busbar, magnetic core or the Asic temperature T° < + 150°C
- Operating temperature range: 40°C < T° < + 125°C
- Output voltage: full ratio-metric (in gain and offset)
- Compact design for PCB mounting.

Advantages

- Excellent accuracy
- Very good linearity
- · Very low thermal offset drift
- Very low thermal gain drift
- Wide frequency bandwidth
- No insertion losses
- Very good ratio size/current range.

Automotive applications

- Starter Generators
- Converters
- Inverters
- Drives.

Principle of HC6F Family

The open loop transducers use an Hall effect integrated circuit.

The magnetic flux density B, contributing to the rise of the Hall voltage, is generated by the primary current $\mathbf{I}_{\rm P}$ to be measured.

The current to be measured I_p is supplied by a current source i.e. battery or generator (Fig. 1).

Within the linear region of the hysteresis cycle, B is proportional to:

B (I_p) = constant (a) x I_p

The Hall voltage is thus expressed by:

 $V_{H} = (R_{H}/d) \times I \times constant (a) \times I_{P}$

Except for \mathbf{I}_{p} , all terms of this equation are constant. Therefore:

 V_{H} = constant (b) x I_{P}

The measurement signal \mathbf{V}_{H} amplified to supply the user output voltage or current.

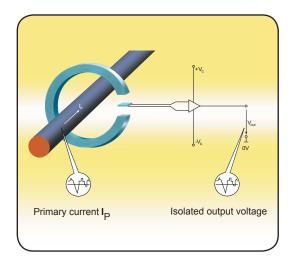
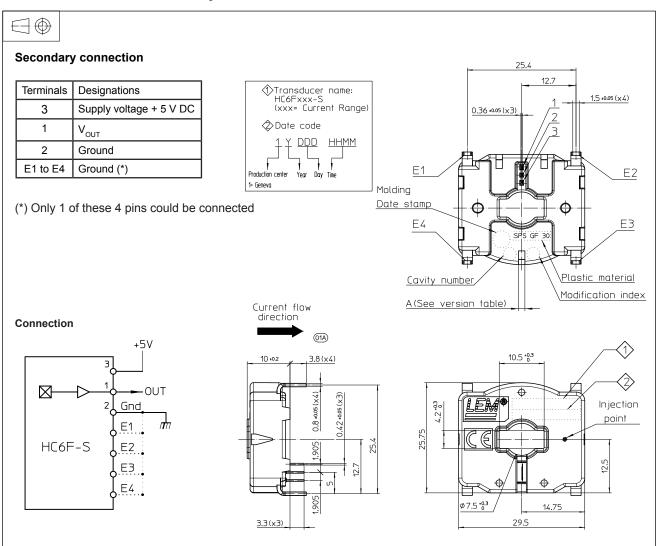


Fig. 1: Principle of the open loop transducer



HC6F300-S

Dimensions HC6F xxx-S family (in mm. 1mm = 0.0394 inch)



Bill of materials

Plastic case
 Magnetic core
 Pins
 Copper alloy tin platted (lead free)

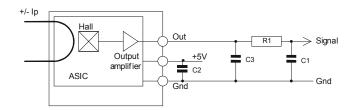
• Mass 23 g

Remarks

• General tolerance ± 0.2 mm

 $\bullet \quad V_{\text{OUT}} > \frac{V_{\text{c}}}{2} \quad \text{ when } I_{\text{p}} \text{ flows in the direction of the arrow}.$

Electronic schematic



Power supply decoupling capacitor: C2 = 47 nF EMC protection capacitor C3 = 4.7 nF

Optional:

High frequency signal noise filter:

 $R1 > 100 \Omega$

C1 = defined according to the system frequency bandwidth

Page 2/5



HC6F300-S

Absolute maximum ratings (not operating)

Parameter	Symbol	Unit	Specification	Conditions
Maximun peak primary current (not operating)	I _{P max}	А	Defined by busbar to have T° ≤ 150°C	
Primary nominal DC or current rms	I _{PN}	А	Defined by busbar to have T° ≤ 150°C	
Maximun supply voltage (not operating)	V _{C max}	V	7	
Secondary maximum admissible power	P _{S max}	mW	150	
Ambient operating temperature	T _A	°C	- 40 < T _A < 125°C	
Ambient storage temperature	T _s	°C	- 55 < T _s < 125°C	see page 5/5
Electrostatic discharge voltage	V _{ESD}	V	2000	see page 5/5
Maximum admissible vibration	γ	m.s -2	100	see page 5/5
Rms voltage for AC isolation test 50 Hz, 1 min	V _d	V	2000	

Operating characteristics

	0		Specification			Canalitiana		
	Symbol	Unit	Min	Typical	Max	Conditions		
Electrical Data								
Primary current, measuring range	I _{PM}	Α	-300	-	300	@ - 40°C < T° < 125°C		
Supply voltage 2)	\mathbf{V}_{c}	V	4.75	5	5.25	@ - 40°C < T° < 125°C		
Output voltage (Analog)	\mathbf{V}_{OUT}	V	$V_{OUT} = V_{C}/5 \times (2.5 + 0.0067 \times I_{P})$.0067 x I _P)	@ - 40°C < T° < 125°C		
Sensitivity	G	V/A	0.0066	0.0067	0.0068	@ T _A = 25°C		
Offset voltage	\mathbf{v}_{\circ}	V	2.471	2.5	2.529	(2) $\mathbf{V}_{C} = 5 \text{ V}; \mathbf{T}_{A} = 25^{\circ}\text{C}; \mathbf{I}_{P} = 0 \text{ A}$		
Current consumption	I _c	mA	-	15	20	@ - 40° C < T° < 125° C; 4.75 V < V_{c} < 5.25 V		
Load resistance	$R_{\scriptscriptstyle L}$	ΚΩ	2	-	-			
Output internal resistance	$\mathbf{R}_{ ext{OUT}}$	Ω	-	-	10			
Performance Data 1)								
Sensitivity error	$\epsilon_{\scriptscriptstyle G}$	%	-2.0	±0.7	2.0	0 T _A = 25°C, V _C = 5 V; Gth = 0.0067		
Electrical offset	I _{OE}	Α	-1.9	±0.7	1.9	$\textcircled{0} \textbf{V}_{\text{C}} = 5 \text{ V}; \ \textbf{T}_{\text{A}} = 25^{\circ}\text{C}$		
	$\mathbf{V}_{\scriptscriptstyle{OE}}$	mV	-12.7	±4.7	12.7			
Magnetic offset	I _{OM}	Α	-2.4	±1.5	2.4	@ After excursion to $\pm I_p$; $T_A = 25^{\circ}C$		
Magnetic onset	$\mathbf{V}_{\scriptscriptstyleOM}$	mV	-16	±10	16	WAItel excursion to £1 _p , 1 _A - 25 C		
Temperature coefficient of	TCI _{OE}	mA/°C	-21	12	+21	@ - 40°C < T° < 125°C; V _C = 5 V		
remperature coefficient of	TCV_OE	mV/°C	-0.14	±0.08	0.14			
Temperature coefficient of G	TCG	%/°C	-0.04	±0.02	0.04	@ - 40° C < T° < 125° C; $V_{_{\rm C}}$ = 5 V		
Linearity error	$\mathcal{E}_{\scriptscriptstyle L}$	% I _P	-1.0	±0.5	1.0	(2) I_p ; $V_C = 5 \text{ V}$, $T_A = 25^{\circ}\text{C}$		
Response time	t,	μs	-	8	15	@ di/dt = 50 A/µs; I _T = 200 A		
Frequency bandwidth 3)	BW	kHz	20	-	-	@ -3 dB; I _T = 40 A rms		
Output voltage noise peak-peak	V _{no p-p}	mV	-	20	25	@ T _A = 25°C; 0 Hz < f < 1 MHz		
Output voltage noise rms	V _{no rms}	mV	-	3	4	@ T _A = 25°C; 0 Hz < f < 1 MHz		

Note: 1) Minimum and maximum values are specified after one operating T° cycle.

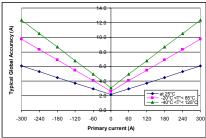


Fig. 2: Typical accuracy versus primary current across temperature



HC6F300-S

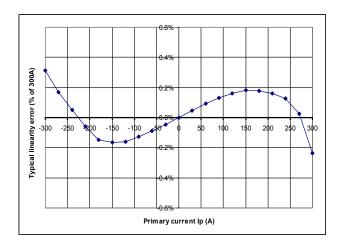
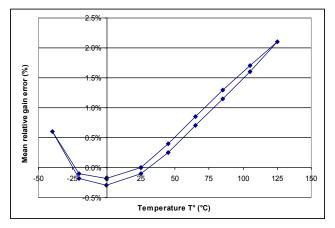


Fig.3: Typical linearity versus primary current at T° = 25°C

Fig. 2: Typical output voltage versus primary current across temperature



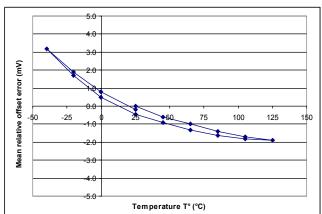


Fig 5: Typical gain error versus temperature

Fig. 6: Typical offset error versus temperature

Influence of the external magnetic field

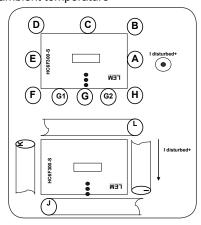
Test conditions:

- tranducers sample: HC6F300-S

- diameter of the disturbing conductor: 6 mm

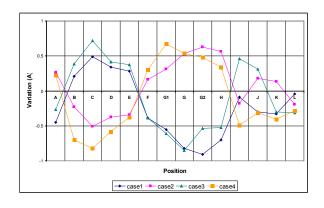
- dimension of the primary busbar: $6 \times 2 \times 200 \text{ mm}$

- tested at ambient temperature



Currents levels

	CASE 1	CASE 2	CASE 3	CASE 4
I disturbant	+ 200 A	-200 A	+ 200 A	-200 A
I primary	0 A	0 A	+ 200 A	+ 200 A



Page 4/5



HC6F300-S PERFORMANCES PARAMETERS DEFINITIONS

Output noise voltage:

The output voltage noise is the result of the noise floor of the Hall elements and the linear ${\bf I}_{\rm c}$ amplifier gain.

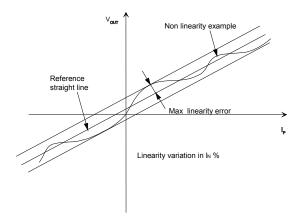
Magnetic offset:

The magnetic offset is the consequence of an over-current on the primary side. It's defined after an excursion of $I_{\rm P\,max}$

Linearity:

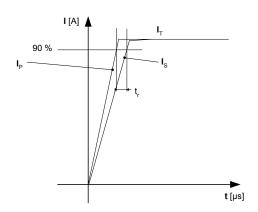
The maximum positive or negative discrepancy with a reference straight line \mathbf{V}_{OUT} = f (\mathbf{I}_{P}).

Unit: linearity (%) expressed with full scale of I_{P max}.



Response time (delay time) t.:

The time between the primary current signal and the output signal reach at 90 % of its final value



Typical:

Theorical value or usual accuracy recorded during the production.

Sensitivity:

The Transducer's sensitivity **G** is the slope of the straight line $V_{out} = f(I_p)$, it must establish the relation:

$$\mathbf{V}_{\text{out}}(\mathbf{I}_{\text{P}}) = \mathbf{V}_{\text{C}}/5 \text{ (G x } \mathbf{I}_{\text{P}} + 2.5) \text{ (*)}$$

(*) For all symetrics transducers.

Offset with temperature:

The error of the offset in the operating temperature is the variation of the offset in the temperature considered with the initial offset at 25°C.

The offset variation I_{OT} is a maximum variation the offset in the temperature range:

$$I_{OT} = I_{OE} \max - I_{OE} \min$$

The Offset drift \mathbf{TCI}_{OEAV} is the \mathbf{I}_{OT} value divided by the temperature range.

Sensitivity with temperature:

The error of the sensitivity in the operating temperature is the relative variation of sensitivity with the temperature considered with the initial offset at 25°C.

The sensitivity variation \mathbf{G}_{T} is the maximum variation (in ppm or %) of the sensitivity in the temperature range:

 $\mathbf{G}_{\scriptscriptstyle T}$ = (Sensitivity max - Sensitivity min) / Sensitivity at 25°C.

The sensitivity drift \mathbf{TCG}_{AV} is the \mathbf{G}_{T} value divided by the temperature range.

Offset voltage @ $I_p = 0$ A:

Is the output voltage when the primary current is null. The ideal value of \textbf{V}_{O} is $\textbf{V}_{\text{C}}/2$ at $\textbf{V}_{\text{C}}=5$ V. So, the difference of \textbf{V}_{O} - $\textbf{V}_{\text{C}}/2$ is called the total offset voltage error. This offset error can be attributed to the electrical offset (due to the resolution of the ASIC quiescent voltage trimming), the magnetic offset, the thermal drift and the thermal hysteresis.

Environmental test specifications

Name	Standard	Conditions			
Low T° storage		T° - 55°C / 64 H not connected			
Thermal shocks	IEC 60068 Part 2-14	T° - 30°C to 110°C /1000 cycles not connected			
Low T°operation at min supply voltage	IEC 60068 Part 2-1	T° - 40°C / 1000 H supply voltage = 4.75 V			
High T° operation at max supply voltage	IEC 60068 Part 2-2	T° 125°C / 1000 H supply voltage = 5.25 V			
Temperature humidity bias	IEC 60068 Part 2-3	T° 90°C / 95 % RH/ 1000 H supply voltage = 5.25 V			
Mechanical Tests					
Vibration	IEC 60068 Part 2-64	Room T°, acceleration 100 m/s2, frequency 20 to 500 Hz/96 H each axis			
Drop test	IEC 60068 Part 2-29	Height 750 mm concrete floor each directions			
EMC Test					
Electrostatic discharge	JESD22-A114-B	Applied voltage = ± 2 kV pin to pin number of discharge = 1			