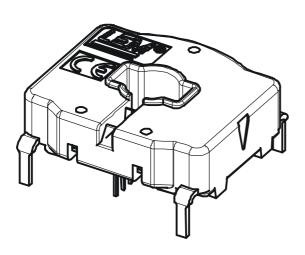


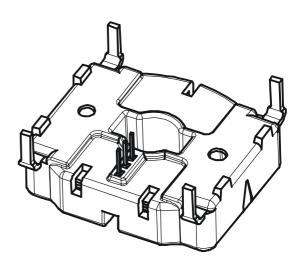
AUTOMOTIVE CURRENT TRANSDUCER HC6F800-S













Introduction

The HC6F Family is for use on the electronic measurement of DC, AC or pulsed currents in high power and low voltage automotive applications with a galvanic isolation between the primary circuit (high power) and the secondary circuit (electronic circuit).

The HC6F family gives you the choice of having different current measuring ranges in the same housing.

Features

- Open Loop transducer using the Hall effect
- Low voltage application
- Unipolar + 5 V DC power supply
- Primary current measuring range from 200 A up to 800 A
- Maximum rms primary admissible current: defined by busbar the magnetic core or the ASIC to have T° < + 150°C
- Operating temperature range: 40°C < T° < + 125°C
- Output voltage: full ratio-metric (in sensitivity and offset)
- Compact design for PCB mounting.

Advantages

- Excellent accuracy
- Very good linearity
- Very low thermal offset drift
- · Very low thermal sensitivity drift
- · Wide frequency bandwidth
- No insertion losses
- Very good ratio size/current range.

Automotive applications

- Starter Generators
- Converters
- Inverters
- Drives.

Principle of HC6F Family

The open loop transducers use an Hall effect integrated circuit. The magnetic induction B, contributing to the rise of the Hall voltage, is generated by the primary current \mathbf{I}_p to be measured. The control current \mathbf{I}_p is supplied by a current source i.e. battery or generator (Fig. 1).

Within the linear region of the hysteresis cycle, B is proportional to:

$$B(I_{p}) = constant(a) \times I_{p}$$

The Hall voltage is thus expressed by:

$$V_{\perp} = (R_{\perp}/d) \times I_{\perp} \times constant (a) \times I_{\perp}$$

Except for \mathbf{I}_{p} , all terms of this equation are constant. Therefore:

$$V_{H}$$
 = constant (b) x I_{D}

The measurement signal \mathbf{V}_{H} amplified to supply the user output voltage or current.

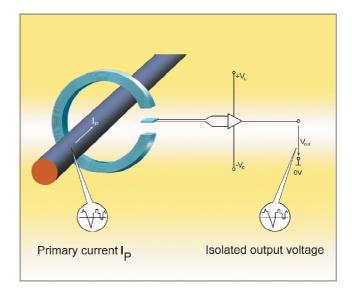


Fig. 1: Principle of the open loop transducer

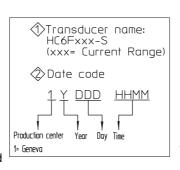


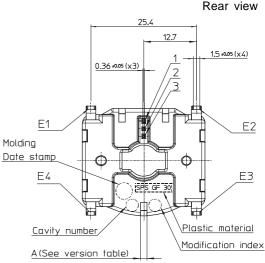
Dimensions HC6F800-S family (in mm. 1mm = 0.0394 inch)

Secondary connection

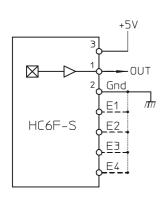
Terminals	Designations
3	Supply voltage + 5 V DC
1	V _{OUT}
2	Ground
E1 to E4	Ground (*)

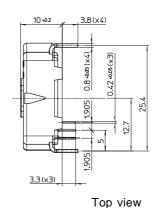
(*) Only 1 of these 4 pins could be connected



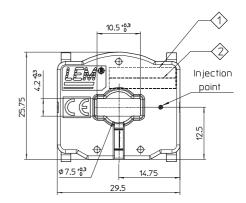


Connection









Right view

Bill of materials

23 g

Plastic case
Magnetic core
Pins
Copper alloy tin platted (lead free)

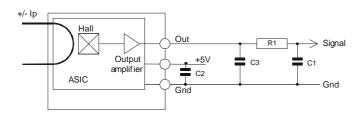
General tolerance

Remarks

± 0.2 mm

• $V_{OUT} > \frac{V_c}{2}$ when I_p flows in the direction of the arrow.

Electronic schematic



Power supply decoupling capacitor: C2 = 47 nFEMC protection capacitor : C3 = 4.7 nF

Optional:

High frequency signal noise filter:

 $R1 > 100 \Omega$

C1 = defined according to the system frequency bandwidth.

Mass



Absolute maximum ratings (not operating)

Parameter	Symbol	Unit	Specification	Conditions
Maximun peak primary current (not operating)	I _{P max}	А	Defined by busbar to have T° ≤ 150°C	
Primary nominal DC or current rms	I _{PN}	Α	Defined by busbar to have T° ≤ 150°C	
Maximun supply voltage (not operating)	V _{C max}	V	7	
Secondary maximum admissible power	P _{S max}	mW	150	
Ambient operating temperature	T _A	°C	- 40 < T _A < 125°C	
Ambient storage temperature	Ts	°C	- 40 < T _S < 125°C	
Electrostatic discharge voltage	V _{ESD}	V	2000	see page 5/5
Maximum admissible vibration	γ	m.s ⁻²	100	see page 5/5
Rms voltage for AC isolation test 50 Hz, 1 min	V _d	V	2000	

Operating characteristics

	Symbol	Symbol Unit		Specification	n	Conditions	
	Symbol	iniboi Unit	Min	Typical	Max	Conditions	
Electrical Data							
Primary current, measuring range	I _{PM}	Α	-800	-	800	@ - 40°C < T° < 125°C	
Supply voltage 1)	V _C	V	4.75	5	5.25	@ - 40°C < T° < 125°C	
Output voltage (Analog)	V _{OUT}	V	$V_{OUT} = V_{OUT}$	$V_{OUT} = V_C/5* (2.5 + 0.0025 \times I_P)$		@ - 40°C < T° < 125°C	
Sensitivity	G	V/A	0.00245	0.00250	0.00255	@ T _A = 25°C	
Offset voltage	V o	V	2.485	2.5	2.515	@ $V_C = 5 \text{ V}; T_A = 25^{\circ}\text{C}; I_P = 0 \text{ A}$	
Current consumption	Ic	mA	-	15	20	@ - 40° C < T° < 125° C; 4.75 V < \textbf{V}_{C} < 5.25 V	
Load resistance	R_L	ΚΩ	2	-	-		
Output internal resistance	R _{OUT}	Ω	-	-	10		
Performance Data							
Sensitivity error	E _G	%	-2.0	±0.7	2.0	@ $T_A = 25$ °C, $V_C = 5$ V; Gth = 0.0025V/A	
Electrical offset	I _{OE}	Α	-3.2	±1.6	3.2	@ V _C = 5 V; T _A = 25°C	
Liectrical offset	V _{OE}	mV	-8	±4	8	₩ V C = 3 V, IA = 23 C	
Magnetic offset	I _{OM}	Α	-2.8	±1.6	2.8	@ After excursion to ± I _P ; T _△ = 25°C	
	V _{OM}	mV	-7	±4	7	C 1 and 1 an	
Temperature coefficient of	TCI _{OE}	mA/°C	-64	±32	64	@ - 40°C < T° < 125°C; V _C = 5 V	
	TCV _{OE}	mV/°C	-0.16	±0.08	0.16		
Temperature coefficient of G	TCG	%/°C	-0.04	±0.02	0.04	@ - 40° C < T° < 125° C; V_{C} = 5 V	
Linearity error	$\epsilon_{\scriptscriptstyle L}$	% I P	-1.0	±0.4	1.0	@ I_P ; $V_C = 5 \text{ V}$, $T_A = 25^{\circ}C$	
Response time	t _r	μs	-	8	15	@ di/dt = 50 A/μs; I _T = 400 A	
Frequency bandwidth 2)	BW	kHz	20	-	-	@ -3 dB; I _T = 100 A rms	
Output voltage noise peak-peak	V _{no p-p}	mV	-	10	13.6	@ T _A = 25°C; 0 Hz < f < 1 MHz	
Output voltage noise rms	V _{no rms}	mV	-	2.5	3.5	@ T _A = 25°C; 0 Hz < f < 1 MHz	

Notes: 1) The output voltage V_{OUT} is fully ratio-metric and depends on the supply voltage V_{C} . The V_{C} value must be measured and used with the following formula:

$$I_P = \left(V_{OUT} - \frac{V_c}{2}\right) \times \frac{1}{G} \times \frac{5}{V_c}$$
 with G in (V/A)

²⁾ Small signal only to avoid excessives heatings of the busbar, the magnetic core and the ASIC.



PERFORMANCE PARAMETERS DEFINITIONS

Sensitivity:

The Transducer's sensitivity **G** is the slope of the straight line $V_{OUT} = f(I_p)$, it must establish the relation:

$$V_{OUT}(I_{P}) = V_{C}/5 (G \times I_{P} + 2.5) (*)$$

* For all symetrics transducers

Offset voltage:

Is the output voltage when the primary current is null. The ideal value of $\mathbf{V}_{\rm O}$ is $\mathbf{V}_{\rm C}/2$. So, the difference of $\mathbf{V}_{\rm O}$ - $\mathbf{V}_{\rm C}/2$ is called the total offset voltage error. This offset error can be attributed to the electrical offset (due to the resolution of the ASIC quiescent voltage trimming), the magnetic offset, the thermal drift and the thermal hysteresis.

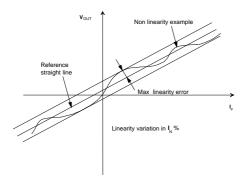
Magnetic offset:

The magnetic offset is the consequence of an over-current on the primary side. It's defined after an excursion of $l_{\rm p}$ max.

Linearity:

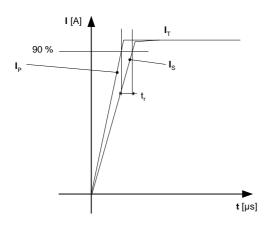
Is the maximum positive or negative discrepancy with a reference straight line $\mathbf{V}_{\text{OUT}} = \mathbf{f}(\mathbf{I}_{\text{p}})$.

Unit: linearity (%) expressed with full scale of I_D max.



Response time (delay time) t.:

Is the time between the primary current signal and the output signal reach at 90 % of its final value.



Output noise voltage:

The output voltage noise is the result of the noise floor of the Hall elements and the linear \mathbf{I}_{c} amplifier sensitivity.

Offset drift:

The error of the offset in the operating temperature \mathcal{E} Offset is the relative variation of the offset in the temperature considered with the initial offset at 25°C. The offset temperature coefficient \mathbf{TCV}_{OE} (\mathbf{TCI}_{OE}) in the operating temperature is the slope off \mathcal{E} Offset = \mathbf{f} (T).

Sensitivity:

The error of the sensitivity in the operating temperature. Sensitivity Error is the relative variation of the sensitivity in the temperature considered with the initial sensitivity at 25°C. Sensitivity temperature coefficient **TCG**.

Typical:

Theorical value or usual accuracy recorded during the production.

Environmental test specifications

Name	Standard	Conditions				
Thermal shocks	IEC 60068 Part 2-14	T° - 40°C to 125°C /1000 cycles not connected				
Low T°operation at min supply voltage	IEC 60068 Part 2-1	T° - 40°C / 1000 H supply voltage = 4.75 V				
High T° operation at max supply voltage	IEC 60068 Part 2-2	T° 125°C / 1000 H supply voltage = 5.25 V				
Temperature humidity bias	IEC 60068 Part 2-3	T° 90°C / 95 % RH/ 1000 H supply voltage = 5.25 V				
Mechanical Tests						
Vibration	IEC 60068 Part 2-64	Room T°, acceleration 100 m/s2, frequency 20 to 500 Hz/96 H each axis				
Drop test	IEC 60068 Part 2-29	Height 750 mm concrete floor each directions				
EMC Test						
Electrostatic discharge	JESD22-A114-B	Applied voltage = ± 2 kV pin to pin number of discharge = 1				